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Institute for form 1449A/PTO				Complete if Known	
				Application Number	10/015,530
				Filing Date	December 12, 2001
				First Named Inventor	Robert T. Plunkett
				Art Unit	2183
				Examiner Name	Aimee Li
Sheet	1	of	2	Attorney Docket Number	046301/046000 (QST-046 US)

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number – Kind Code ² (if known)	Publication Date MM-DD-YYYY			
	1.	US 4,380,046	04-12-1983	Fung		
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Examiner Signature		Date Considered	
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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /AL/

Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	10/015,530
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		Country Code ⁴	Kind Code ⁵ (if known)				
44.	WO 00/69073		A1	11-16-2000	Wasson		

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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	48.	Janweijer et al., "A Compact Robin Using the SHarc (CRUSH)," Obtained from: http://www.nikhef.nl/~peterj/Crush/CRUSH-hw.pdf (Sept 1998)	
	49.	Rajagopalan et al., "A Flexible Multiplication Unit for an FPGA Logic Block," <i>Circuits and Systems</i> 4:546-9 (2001)	
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*Examiner Signature	/Aimee Li/	Date Considered	06/11/2009
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